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MASTER OF ENGINEERING SCIENCE IN WIRELESS AND NETWORK ENGINEERING

COURSE CODE: EMWN
CRICOS CODE: 065106G

The objective of the course is to provide opportunities for suitably qualified persons to acquire skills and expertise necessary to undertake research and development in the field of Wireless and Network engineering. It is one of the most exciting and self-satisfying professional engineering careers. It helps people communicate through optical fibres, satellites, mobile phone and the Internet. It also provides people with the means to entertain through radio, television and multimedia. Similarly, it enables machines to network and talk to each other, thus providing the very foundation for modern industry and business.

Wireless and Network engineers involve themselves in solving interesting problems such as how to transmit thousands of telephone calls simultaneously without mixing them up, how to receive signals back from Mars, or how to bring high-speed Internet to the home.

WHERE DO I STUDY?
The course is located at the Footscray Park Campus.

WHAT DO I GET WHEN I FINISH STUDY?
When you complete your postgraduate study, you can pursue a career in senior positions in Telecommunication engineering industries.

WHAT IS THE COURSE STRUCTURE AND WHAT IS INVOLVED
The course is of one year duration for full-time students and a part-time equivalent for part-time students.

The course is unit based in which 8 core units must be completed to successfully graduate from this course. A unit is worth 12 credit points and all 8 units must be completed. Course material is drawn from a variety of backgrounds that include wireless and personal mobile communication, theory, modelling and networking. The course aims to produce engineers with the necessary skills and practical experience to satisfy the requirements of the industry.

CORE UNITS OF STUDY SEMESTER ONE
VET6510 COMMUNICATION THEORY
VET6501 COMMUNICATION SYSTEM MODELING AND SIMULATION 1
VET6531 WIRELESS COMMUNICATION SUBSYSTEMS
VET6511 PRINCIPLES DATA NETWORK ANALYSIS AND DESIGN

CORE UNITS OF STUDY SEMESTER TWO
VET6520 DIGITAL COMMUNICATION
VET6562 DIGITAL SIGNAL PROCESSING
VET6502 COMMUNICATION SYSTEM MODELING AND SIMULATION 2
VET6542 MOBILE AND PERSONAL COMMUNICATION SYSTEMS

ASSESSMENT
Assessment will be based on a combination of written assignments, laboratory exercises, project work, and formal examinations.

ACADEMIC YEAR
The normal academic year commences in late February and extends through to the examination period in late November. There are two intakes, the first one in March and the second in late July.

WHAT PRE-REQUISITES DO I NEED?
Admission to the course requires a four year Bachelor of Engineering degree in Electrical & Electronic Engineering or a four year Bachelor of Applied Science (Honours) degree in an appropriate field, or an equivalent.

Applicants with a three year Bachelor of Applied Science degree (in an appropriate field) or a Bachelor of Engineering degree in another field may also be considered for admission on the condition that they may be required to complete some preliminary subjects that will strengthen their knowledge and skills in micro/nano electronic engineering.

INTERNATIONAL STUDENTS
Full-Fee paying international students are required to have qualifications equivalent to those above, and in addition, they must provide evidence of proficiency in English Language, as assessed by:

- International English Language Testing System (IELTS) — an overall band score of 6+ subject to individual profile,
or,
- Test of English as a Foreign Language — a score of 550+, and a Test of Written English score of 5+.

FEES
A limited number of Commonwealth Australia HECS-HELP places are available for Australia students. International students, please see the International course guide on the VU website for the most up to date fees.